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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/803,117 03/10/2001 PAT 6,686,252

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY VT	SHEETS DRAWING 11	TOTAL CLAIMS 26	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>	

ADDRESS

24241

IBM MICROELECTRONICS

INTELLECTUAL PROPERTY LAW

1000 RIVER STREET

972 E

ESSEX JUNCTION, VT

05452

TITLE

Method and structure to reduce CMOS inter-well leakage

FILING FEE RECEIVED 878	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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